



## **PTC thermistors for overcurrent protection**

Leaded disks, coated, 12 V

**Series/Type:** B599\*5  
**Date:** March 2006

## Overcurrent protection

Leaded disks, coated, 12 V

C935 ... C995

### Applications

- Overcurrent and short-circuit protection

### Features

- Lead-free terminals
- Manufacturer's logo and type designation stamped on in yellow
- Low resistance
- For rated currents of up to 2.1 A
- High thermal stability
- UL approval to UL 1434 (file number E69802)
- VDE approval (license number 104843 E)
- RoHS-compatible

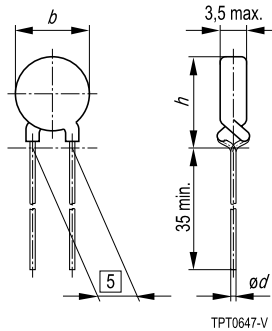
### Options

- Leadless disks and leaded disks without coating available on request
- Thermistors with diameter  $b \leq 11.0$  mm are also available on tape (to IEC 60286-2)

### Delivery mode

- Cardboard strips (standard)
- Cardboard tape reeled or in Ammo pack on request

### Dimensional drawing



### Dimensions (mm)

Type	$b_{max}$	$h_{max}$	$\varnothing d$
C935	22.0	25.5	0.6
C945	17.5	21.0	0.6
C955	13.5	17.0	0.6
C965	11.0	14.5	0.6
C975	9.0	12.5	0.6
C985	6.5	10.0	0.6
C995	4.0	7.5	0.5

### General technical data

Max. operating voltage	$(T_A = 60\text{ °C})$	$V_{max}$	20	VDC or VAC
Rated voltage		$V_R$	12	VDC or VAC
Switching cycles		N	100	
Reference temperature	(typ.)	$T_{ref}$	160	°C
Tolerance of $R_R$		$\Delta R_R$	$\pm 25$	%
Operating temperature range	$(V = 0)$	$T_{op}$	-40/+125	°C
Operating temperature range	$(V = V_{max})$	$T_{op}$	-40/+85	°C

**Overcurrent protection**
**Leaded disks, coated, 12 V**
**C935 ... C995**
**Electrical specifications and ordering codes**

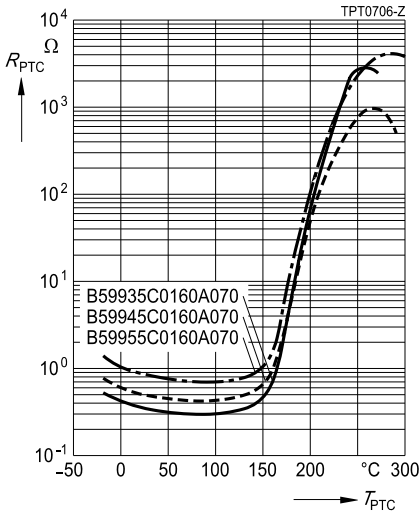
Type	$I_R$ mA	$I_S$ mA	$I_{Smax}$ ( $V = V_{max}$ ) A	$I_r$ ( $V = V_{max}$ ) typ. mA	$I_r$ ( $V = V_R$ ) typ. mA	$R_R$ $\Omega$	$R_{min}$ $\Omega$	Ordering code
C935	2100	4150	10.0	240	380	0.3	0.2	B59935C0160A070
C945	1500	3050	8.0	170	270	0.45	0.3	B59945C0160A070
C955	950	1900	5.5	120	190	0.8	0.5	B59955C0160A070
C965	700	1450	4.3	105	165	1.2	0.7	B59965C0160A070
C975	550	1100	3.0	85	135	1.8	1.1	B59975C0160A070
C985	300	600	1.0	65	100	4.6	2.7	B59985C0160A070
C995	150	300	0.7	40	65	13	7.8	B59995C0160A070

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**Reliability data**

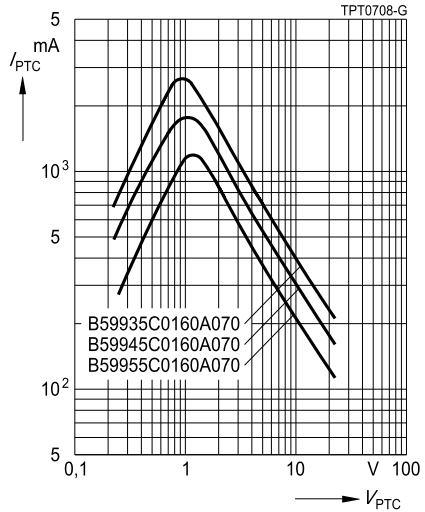
Test	Standard	Test conditions	$ \Delta R_{25}/R_{25} $
Electrical endurance, cycling	IEC 60738-1	Room temperature, $I_{Smax}, V_{max}$ Number of cycles: 100	< 25%
Electrical endurance, constant	IEC 60738-1	Storage at $V_{max}/T_{op}$ Test duration : 1000 h	< 25%
Damp heat	IEC 60738-1	Temperature of air: 40 °C Relative humidity of air: 93% Duration: 56 days Test according to IEC 60068-2-78	< 10%
Rapid change of temperature	IEC 60738-1	$T = T_{LCT}, T = T_{UCT}$ Number of cycles: 5 Test duration: 30 min Test according to IEC 60068-2-14, Test Na	< 10%
Vibration	IEC 60738-1	Frequency range: 10 to 55 Hz Displacement amplitude: 0.75 mm Test duration: 3 · 2 h Test according to IEC 60028-2-6, Test Fc	< 5%
Bump	IEC 60738-1	Pulse shape: half-sine Acceleration: 50 g Pulse duration: 1 ms; 6 · 3 pulses Test according to IEC 60068-2-29	< 5%
Climatic sequence	IEC 60738-1	Dry heat: $T = T_{UCT}$ Test duration: 16 h Damp heat first cycle Cold: $T = T_{LCT}$ Test duration: 2 h Damp heat 5 cycles Tests performed according to IEC 60068-2-30	< 10%

**Characteristics (typical)**

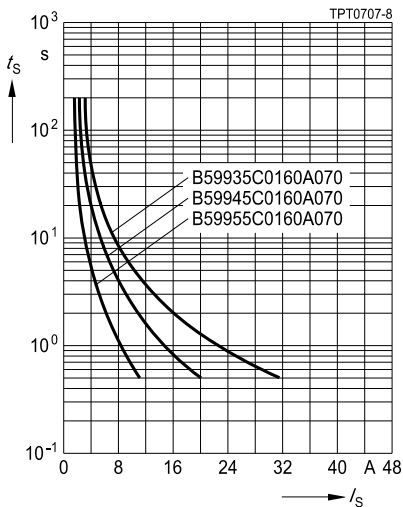
PTC resistance  $R_{PTC}$  versus  
PTC temperature  $T_{PTC}$   
(measured at low signal voltage)



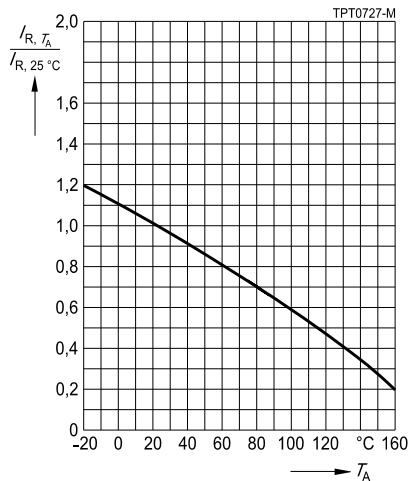
PTC current  $I_{PTC}$  versus PTC voltage  $V_{PTC}$   
(measured at 25 °C in still air)



Switching time  $t_s$  versus switching current  $I_s$   
(measured at 25 °C in still air)

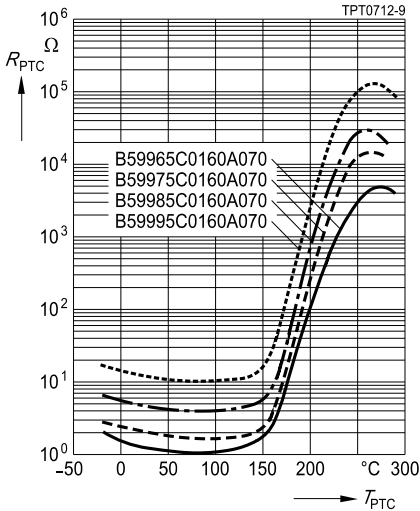


Rated current  $I_R$  versus ambient temperature  $T_A$   
(measured in still air)

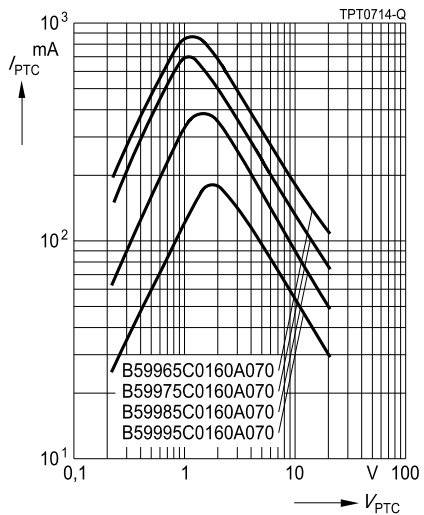


**Characteristics (typical)**

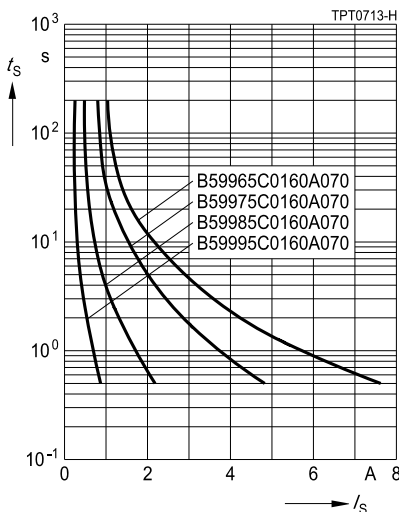
PTC resistance  $R_{PTC}$  versus  
PTC temperature  $T_{PTC}$   
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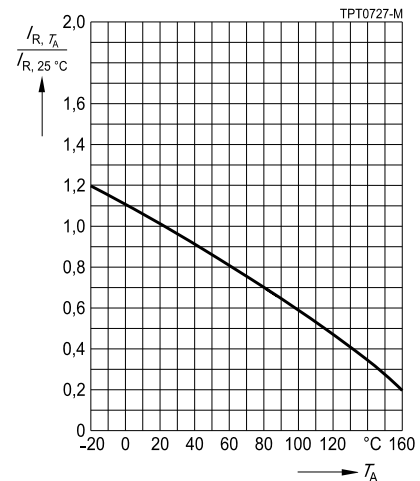
PTC current  $I_{PTC}$  versus PTC voltage  $V_{PTC}$   
(measured at 25 °C in still air)



Switching time  $t_s$  versus switching current  $I_s$   
(measured at 25 °C in still air)



Rated current  $I_R$  versus ambient temperature  $T_A$   
(measured in still air)



## Cautions and warnings

### General

- EPCOS thermistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of thermistor through reliability testing during the design-in phase. The thermistors should be evaluated taking into consideration worst-case conditions.

### Storage

- Store thermistors only in original packaging. Do not open the package before storage.
- Storage conditions in original packaging: storage temperature  $-25\text{ °C} \dots +45\text{ °C}$ , relative humidity  $\leq 75\%$  annual mean, maximum 95%, dew precipitation is inadmissible.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environment with effect on function on long-term operation (examples given under operation precautions).
- Use thermistor within 6 months after delivery.

### Handling

- PTCs must not be dropped. Chip-offs must not be caused during handling of PTCs.
- Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

### Soldering

- Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.

### Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housing used for assembly with thermistor have to be clean before mounting. Especially grease or oil must be removed.
- When PTC thermistors are encapsulated with sealing material, the precautions given in chapter "Mounting instructions", "Sealing and potting" must be observed.
- When the thermistor is mounted, there must not be any foreign body between the electrode of the thermistor and the clamping contact.
- The minimum force of the clamping contacts pressing against the PTC must be 10 N.
- During operation, the thermistor's surface temperature can be very high. Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling at the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of thermistor surface during processing.

### Operation

- Use thermistors only within the specified temperature operating range.
- Use thermistors only within the specified voltage and current ranges.
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by abnormal function (e.g. use VDR for limitation of overvoltage condition).



## Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of passive electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of a passive electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of a passive electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
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